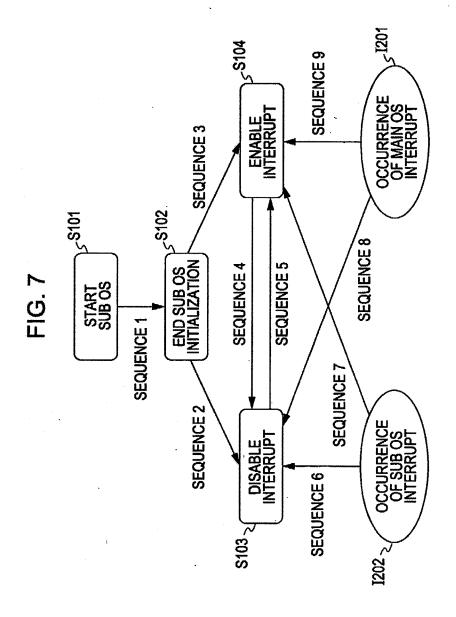


FIG. 6

OS TYPE	INTERRUPT ENABLED STATE OR INTERRUPT DISABLED STATE	RESERVE INTERRUPT	PROCESS INTERRUPT
MAIN OS	DISABLED	I	1
SUB OS-1	ENABLED	1	INTERRUPT e
SUB OS-2	DISABLED	INTERRUPT a INTERRUPT b	l
	•	• •	. • •
SUB OS-n	DISABLED	INTERRUPT c INTERRUPT d	I



REPLACEMENT SHEET

FIG. 8

SCENARIO	OS IN OPERATION	INTERRUPT PRIORITY	INTERRUPT DESTINATION OS	INTERRUPT ENABLED/DISABLED STATE OF INTERRUPT DESTINATION OS
SCENARIO 01	SO BOS	LOW	MAIN OS	INTERRUPT ENABLED
SCENARIO 02	SO BUS	MOT	MAIN OS	INTERRUPT DISABLED
SCENARIO 03	SO BUS	MOT	SO BUS	INTERRUPT ENABLED
SCENARIO 04	SO BOS	MOT	SO B OS	INTERRUPT DISABLED
SCENARIO 05	SO BUS	H9IH	MAIN OS	INTERRUPT ENABLED
SCENARIO 06	SO BOS	H9IH	MAIN OS	INTERRUPT DISABLED
SCENARIO 07	SO BOS	HIGH	SUB OS	INTERRUPT ENABLED
SCENARIO 08	SO BUS	HЭIH	SO BUS	INTERRUPT DISABLED
SCENARIO 09	MAIN OS	MOT	MAIN OS	INTERRUPT ENABLED
SCENARIO 10	MAIN OS	MOT	MAINOS	INTERRUPT DISABLED
SCENARIO 11	MAIN OS	MOT	SO BOS	INTERRUPT ENABLED
SCENARIO 12	MAIN OS	MOT	SO BUS	INTERRUPT DISABLED
SCENARIO 13	MAIN OS	HIGH	MAIN OS	INTERRUPT ENABLED
SCENARIO 14	MAIN OS	HIGH	MAIN OS	INTERRUPT DISABLED
SCENARIO 15	MAIN OS	HIGH	SO BUS	INTERRUPT ENABLED
SCENARIO 16	MAIN OS	HIGH	SUB OS	INTERRUPT DISABLED

FIG. 9

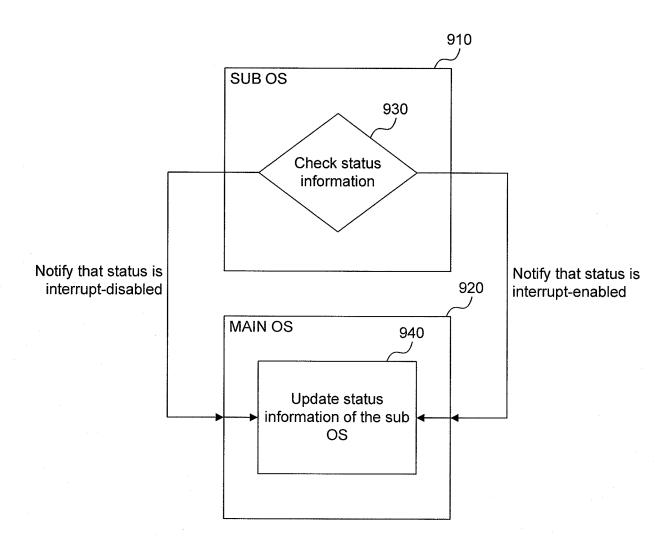


FIG. 10

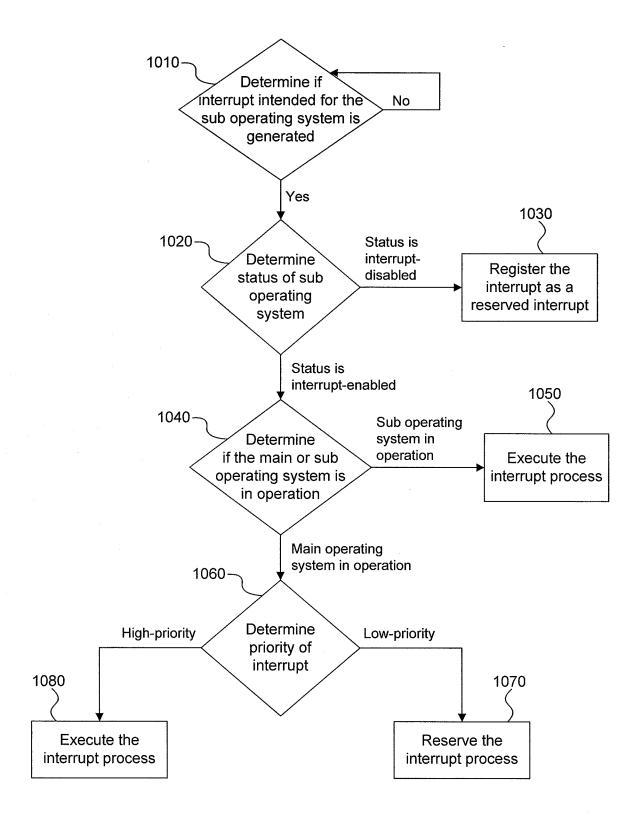


FIG. 11

